

SCHEMATIC FOR GNSS EVAL KIT PNT-SG3FS, PNT-SG4FM

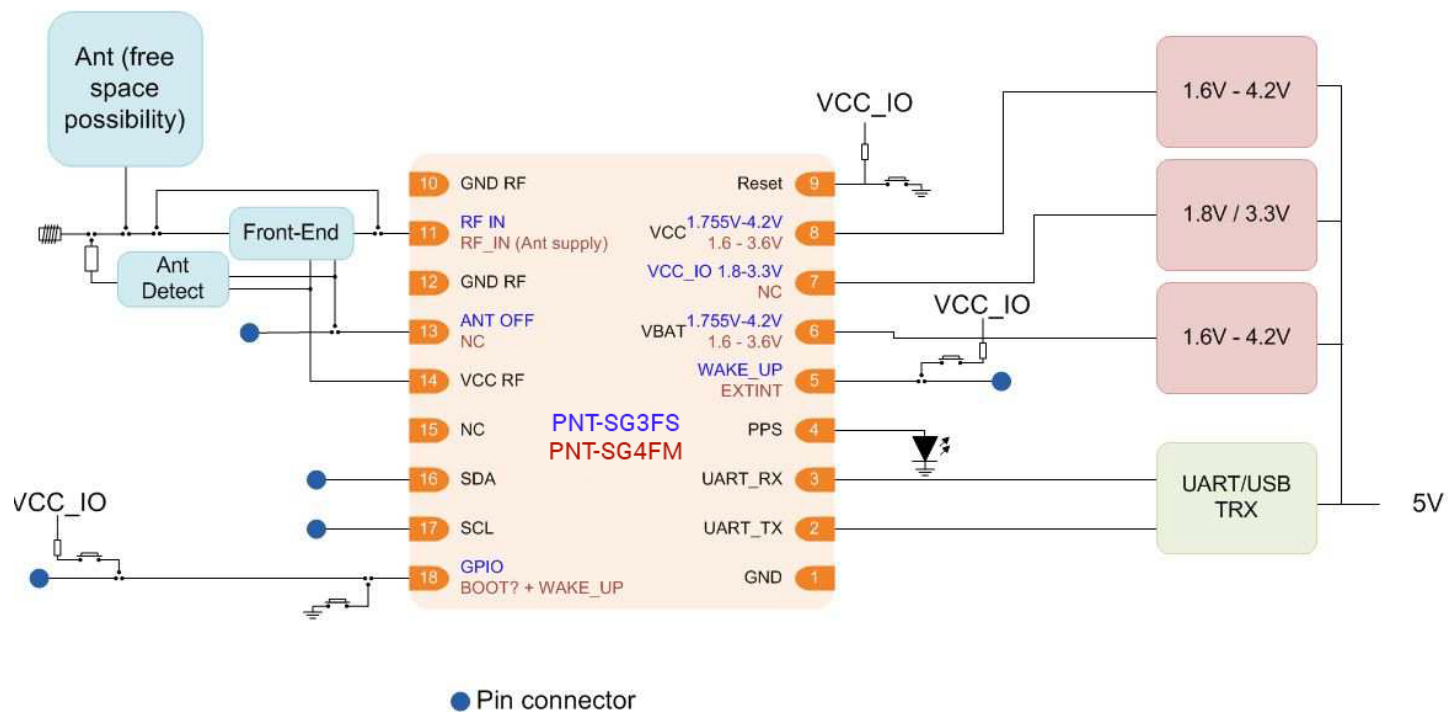
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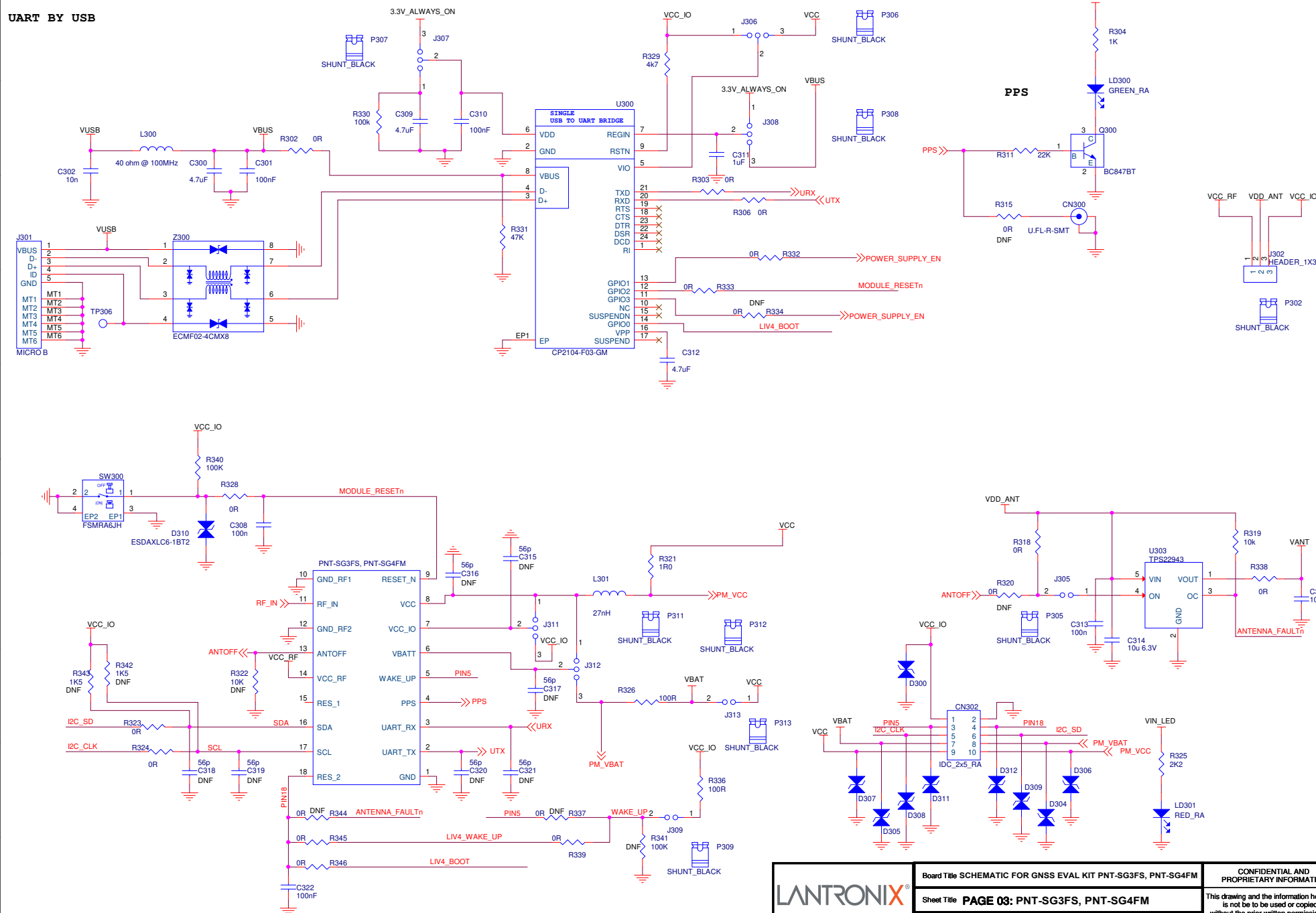
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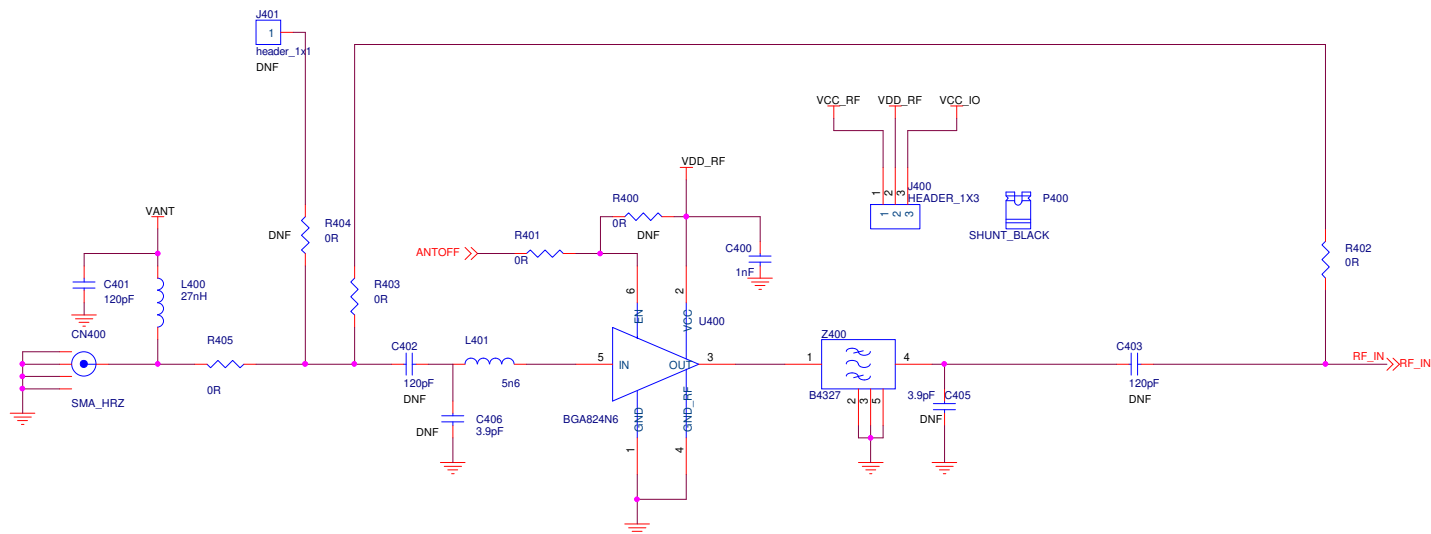
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Bloc diagram



UART BY USB

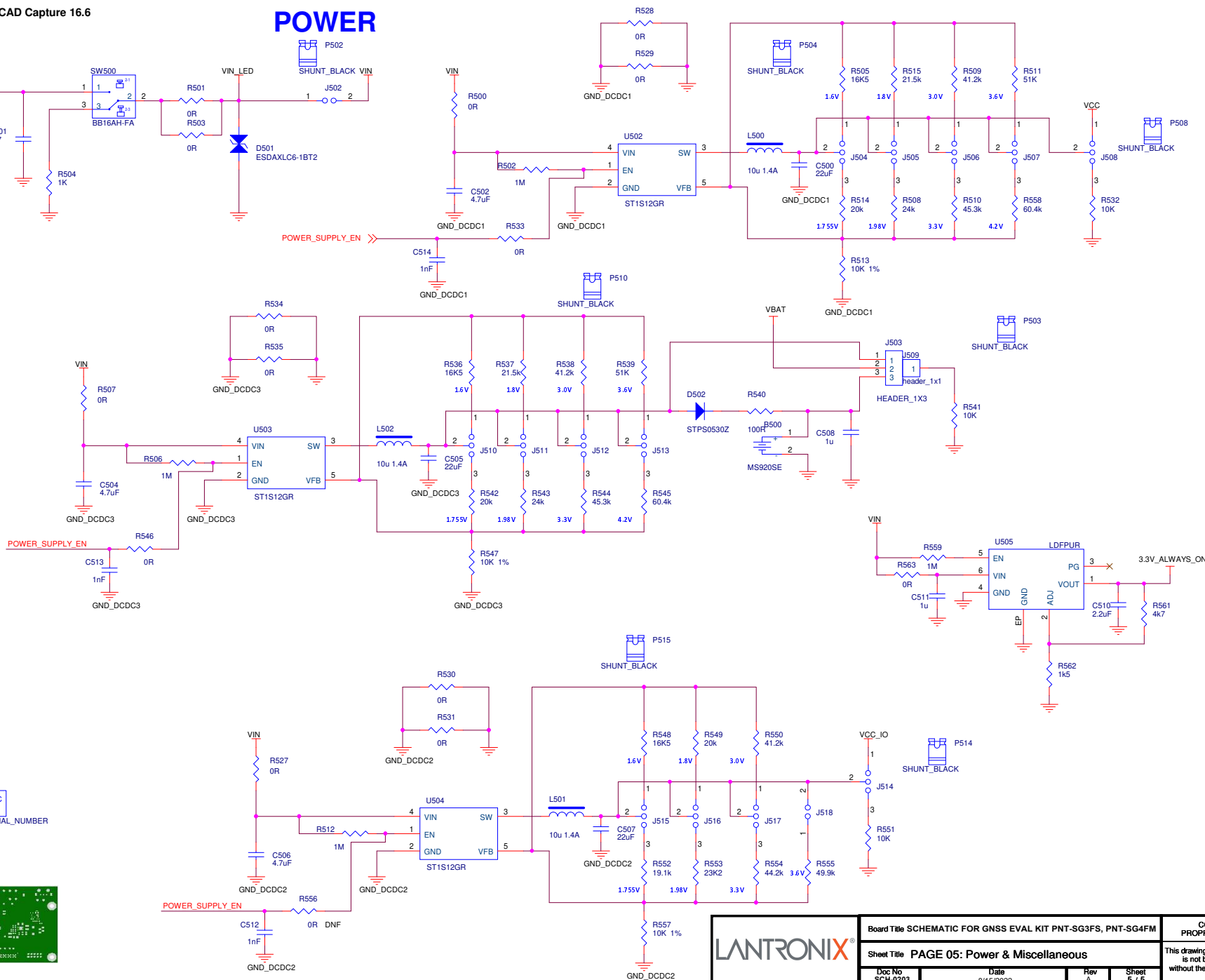
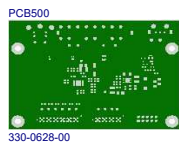




Designed in OrCAD Capture 16.6

POWER

HW500
XXXXY-AABBCC
12345678-123
HW_LABEL_SERIAL_NUMBER



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